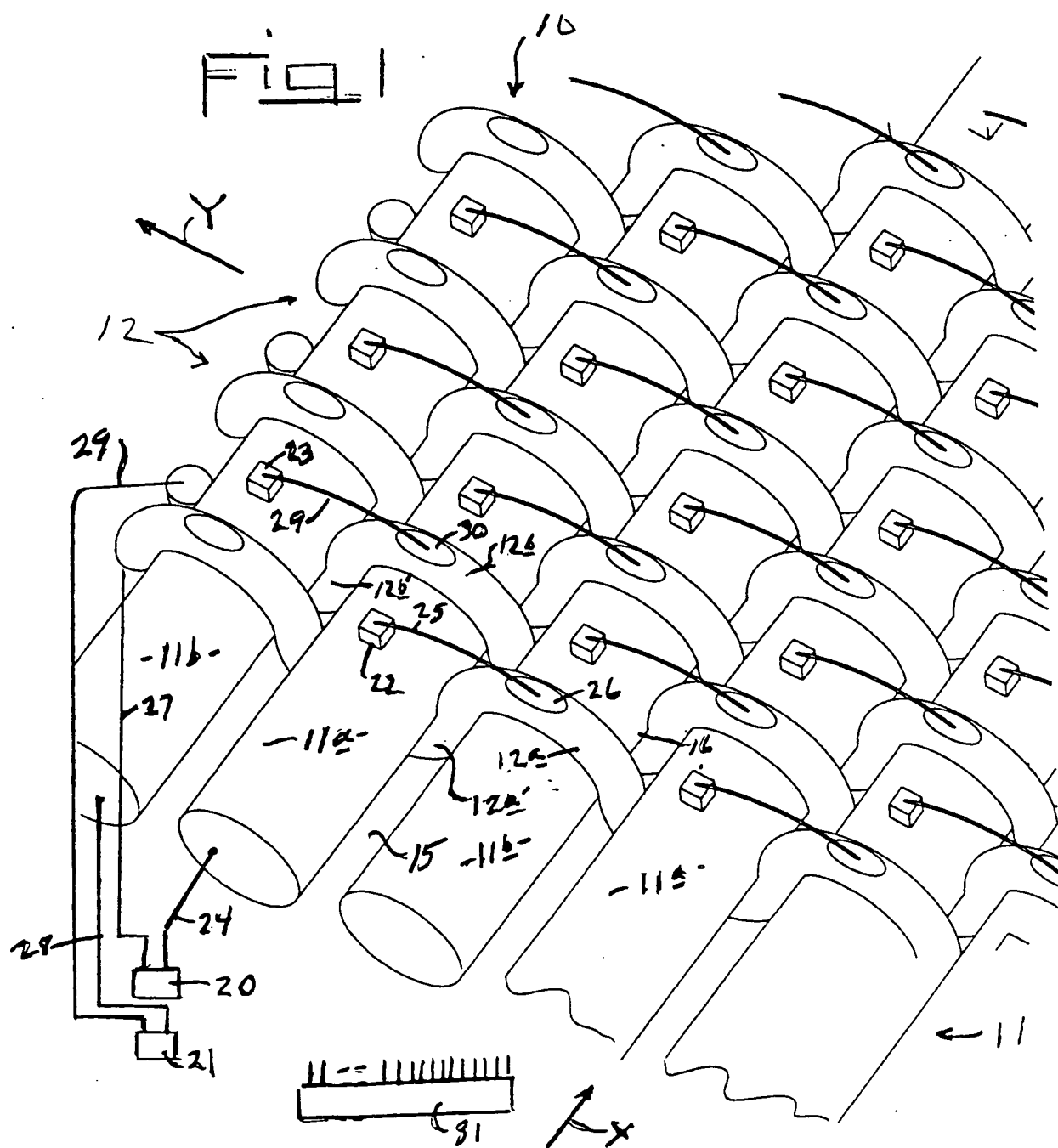
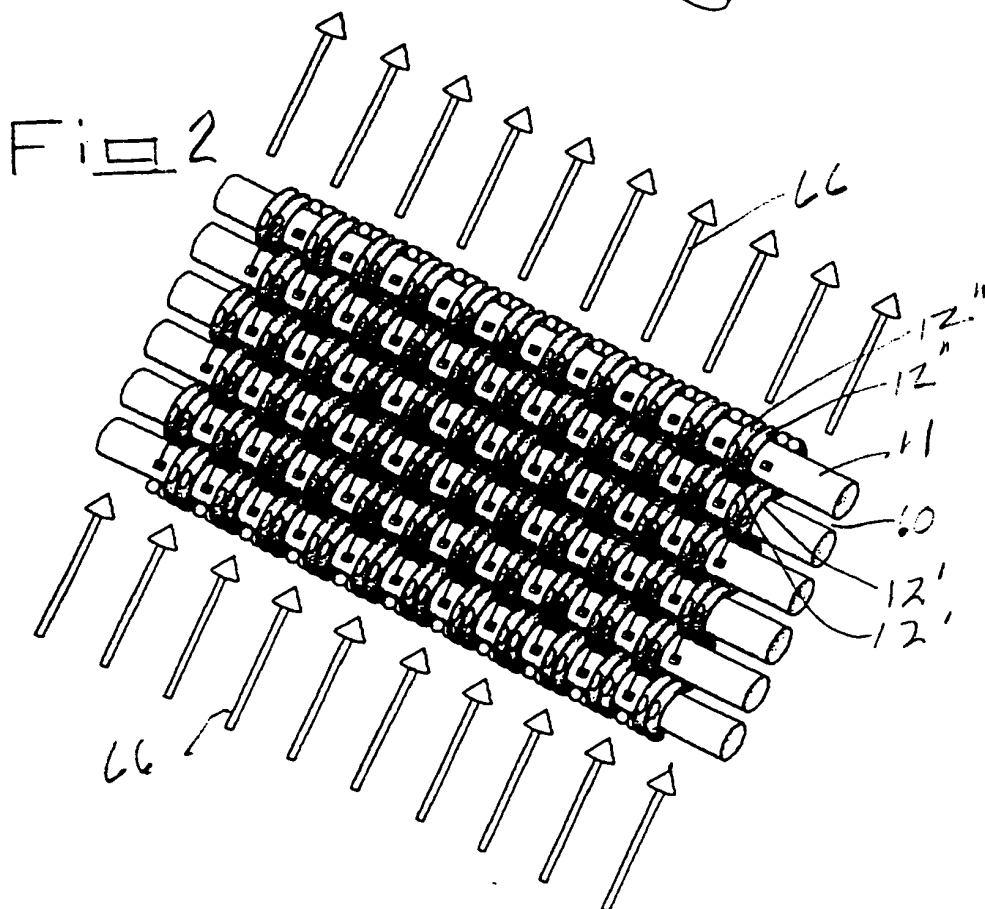
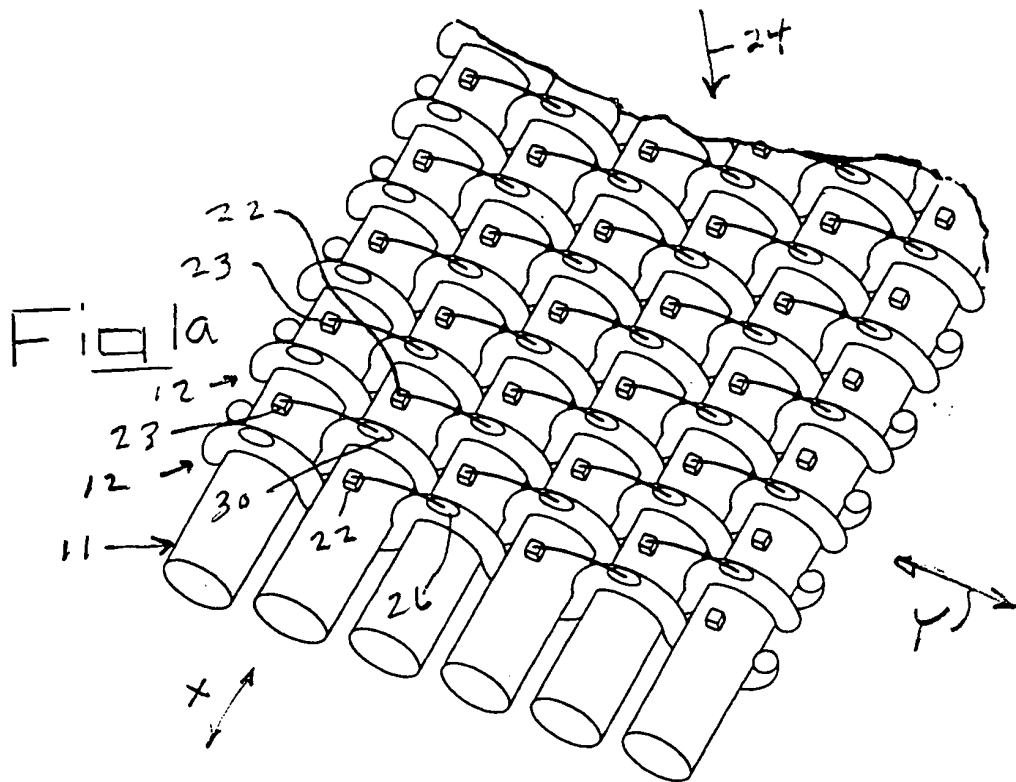


1-28



2-28



3-28

Fig 3

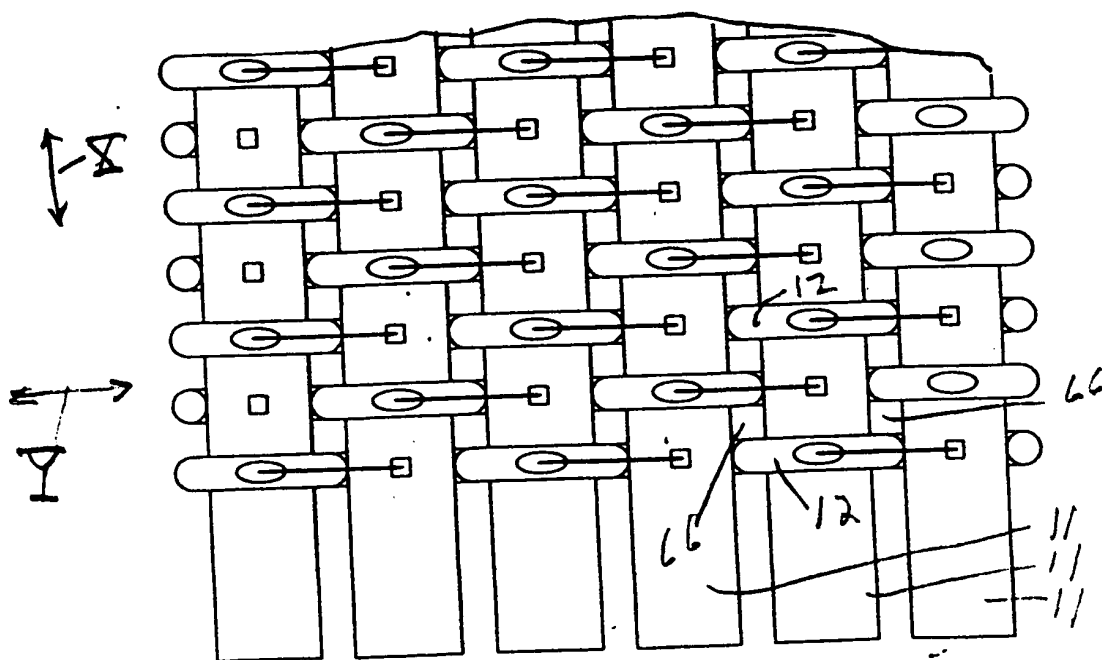
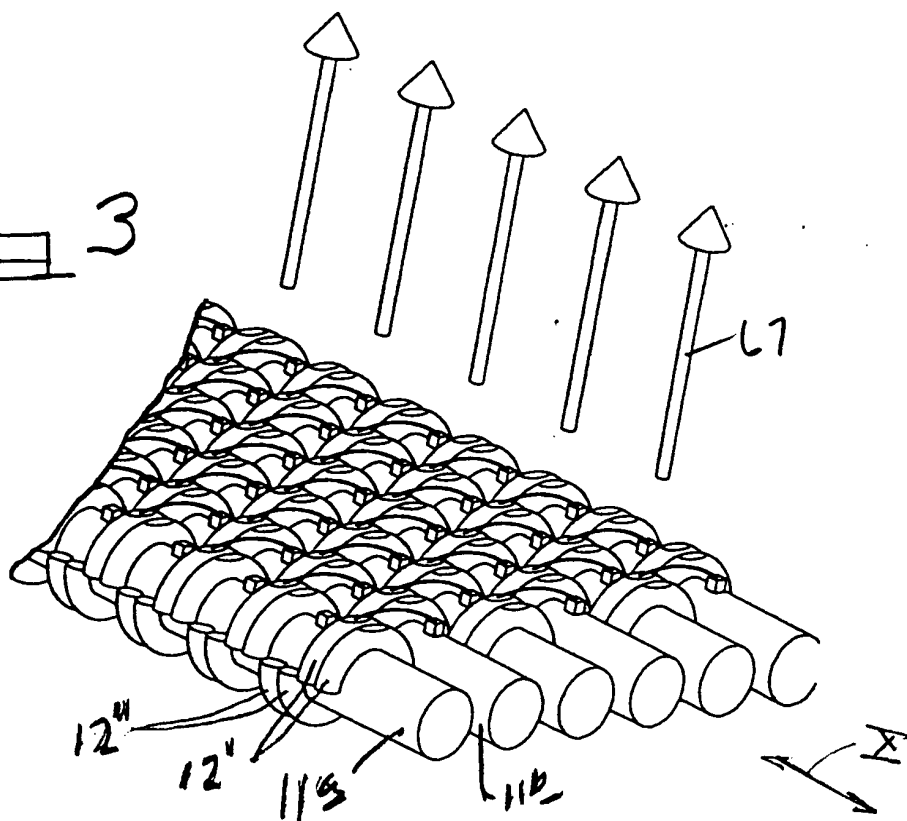
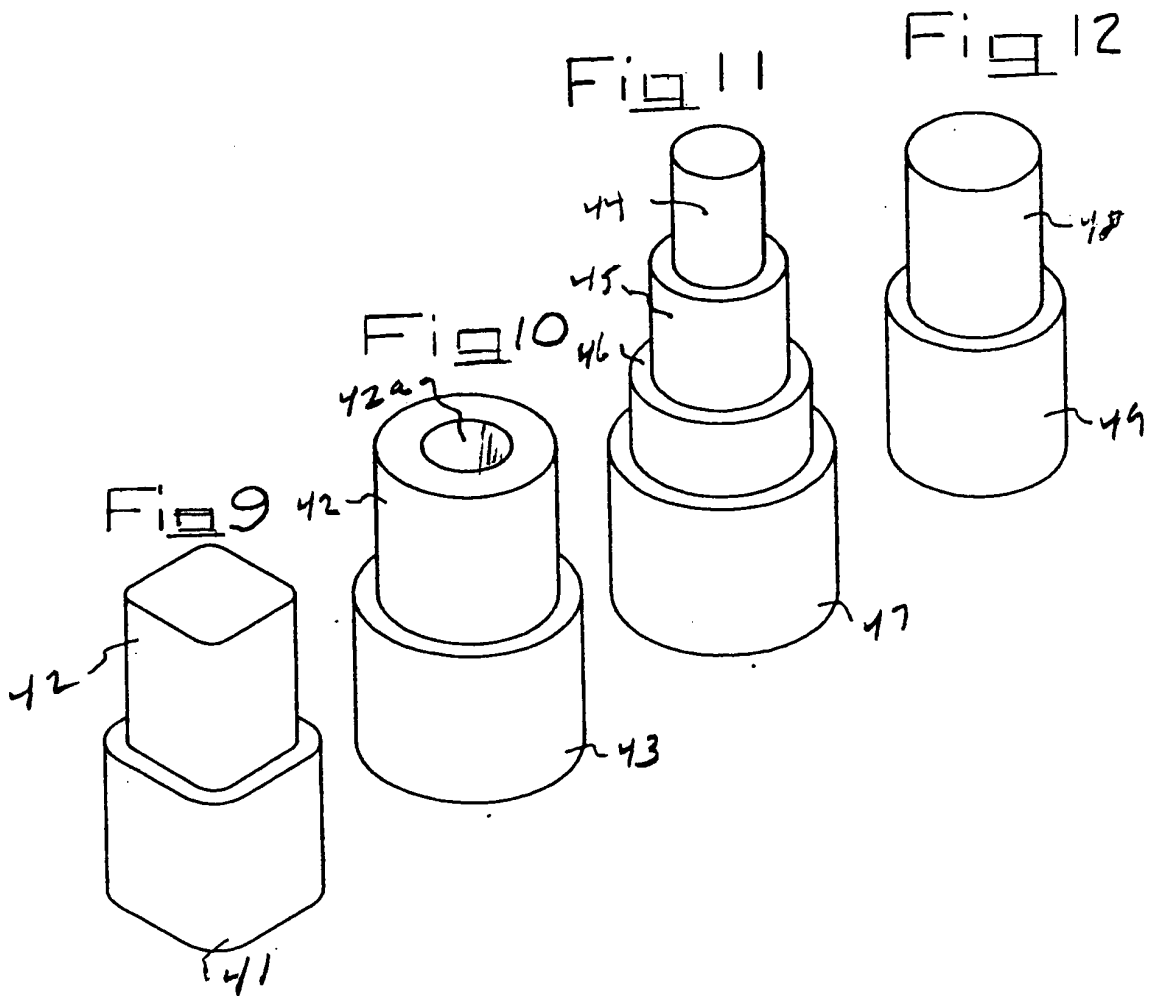
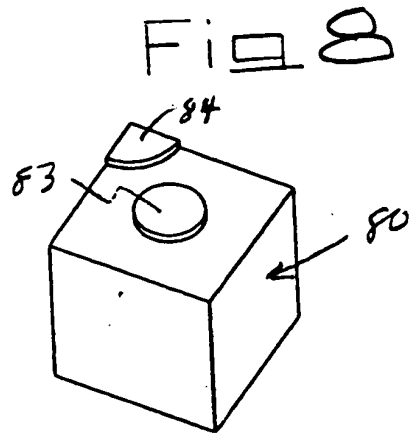
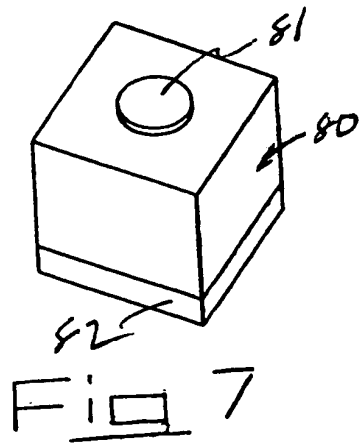


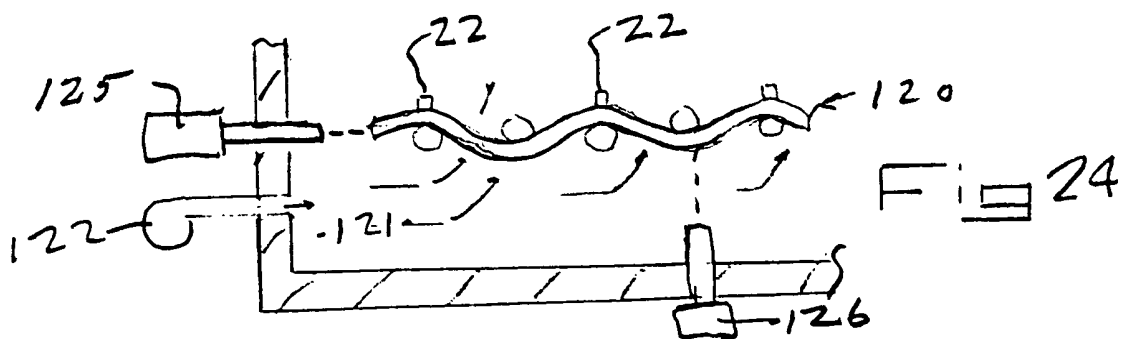
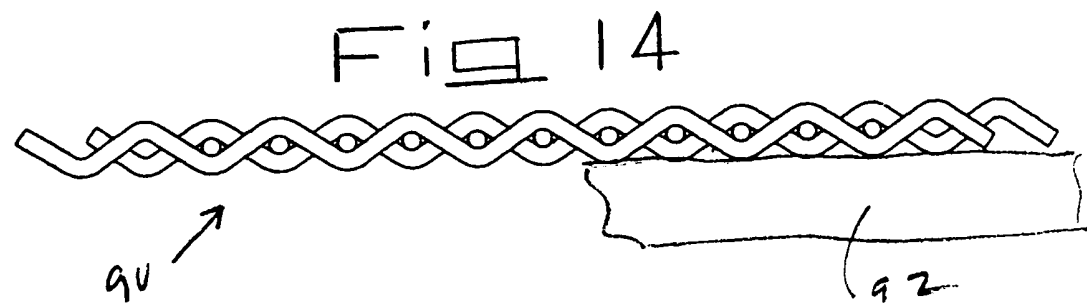
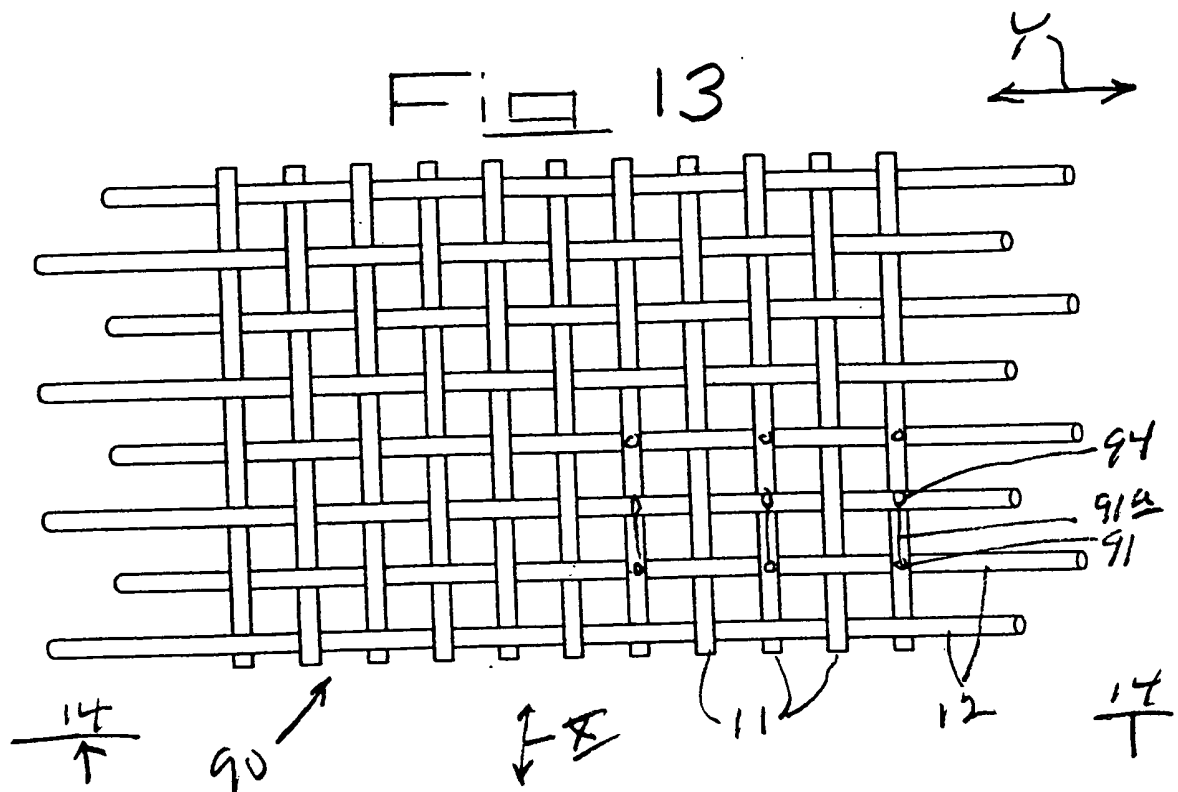
Fig 4



5-28



6-28



7-28

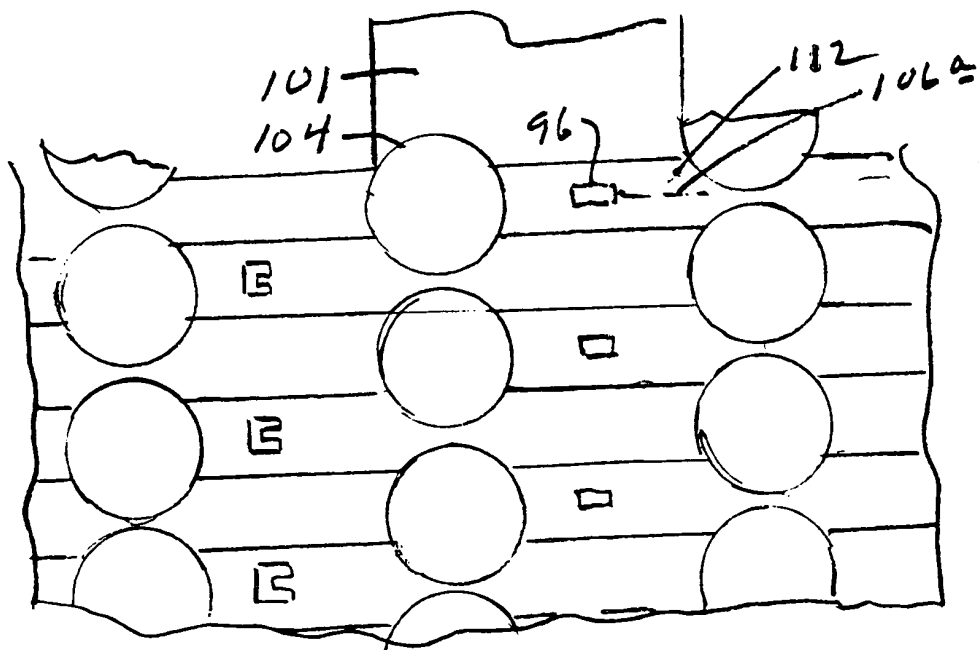
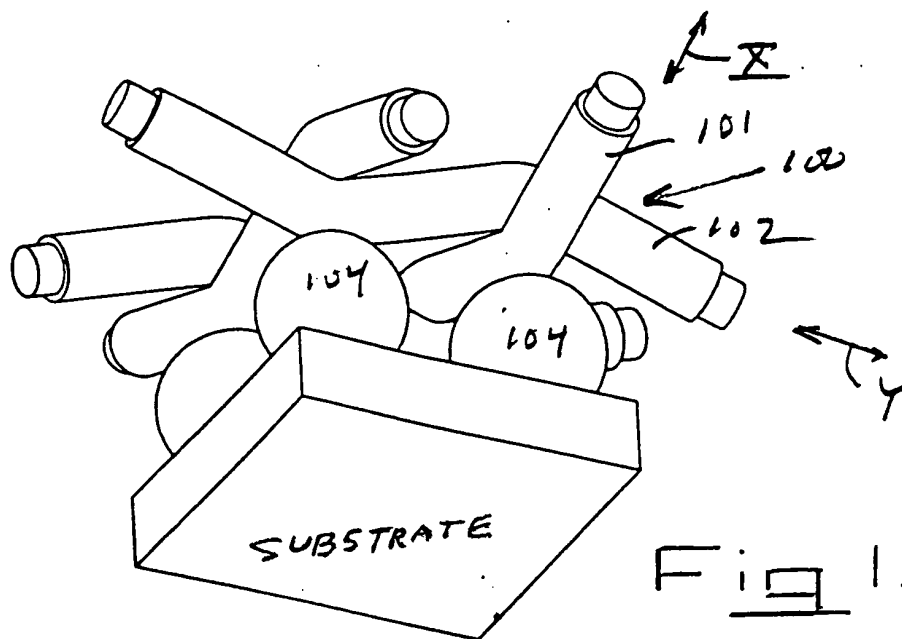
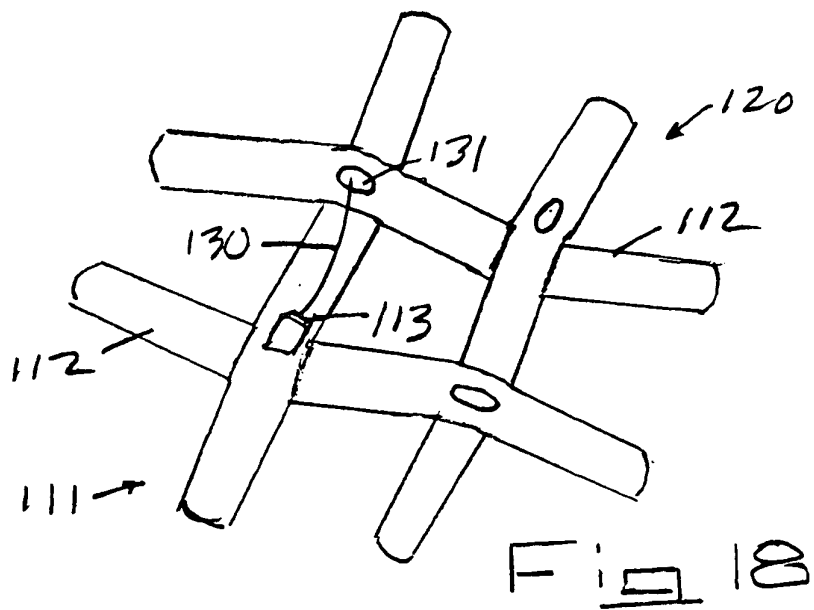
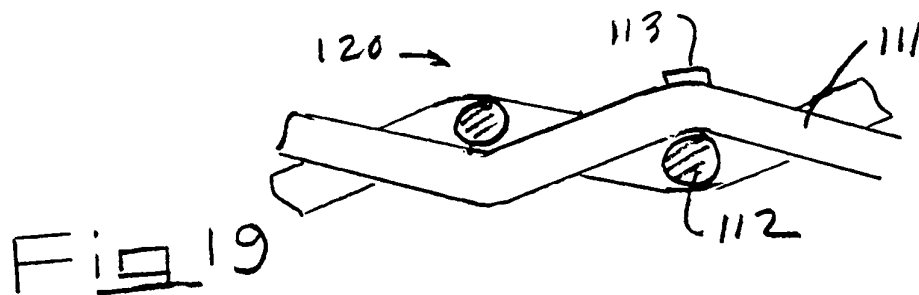
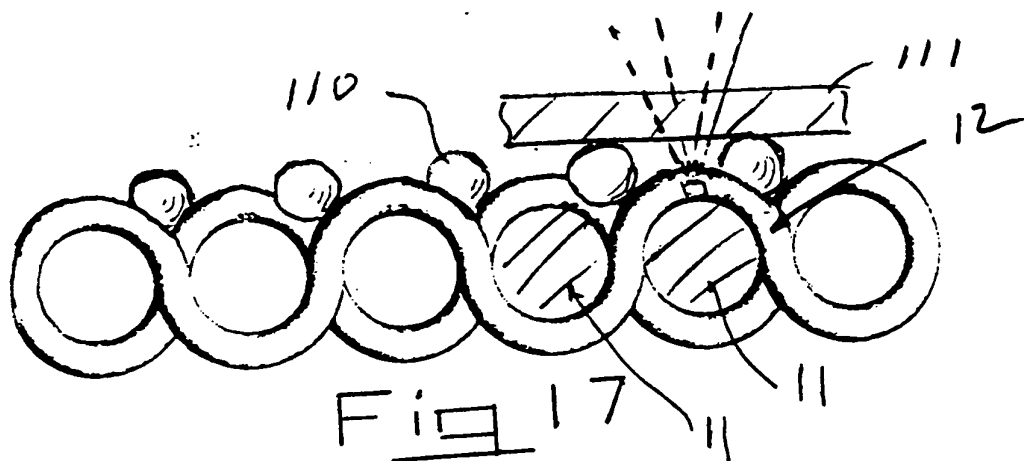


Fig 16

8-28





9-28

Fig 20

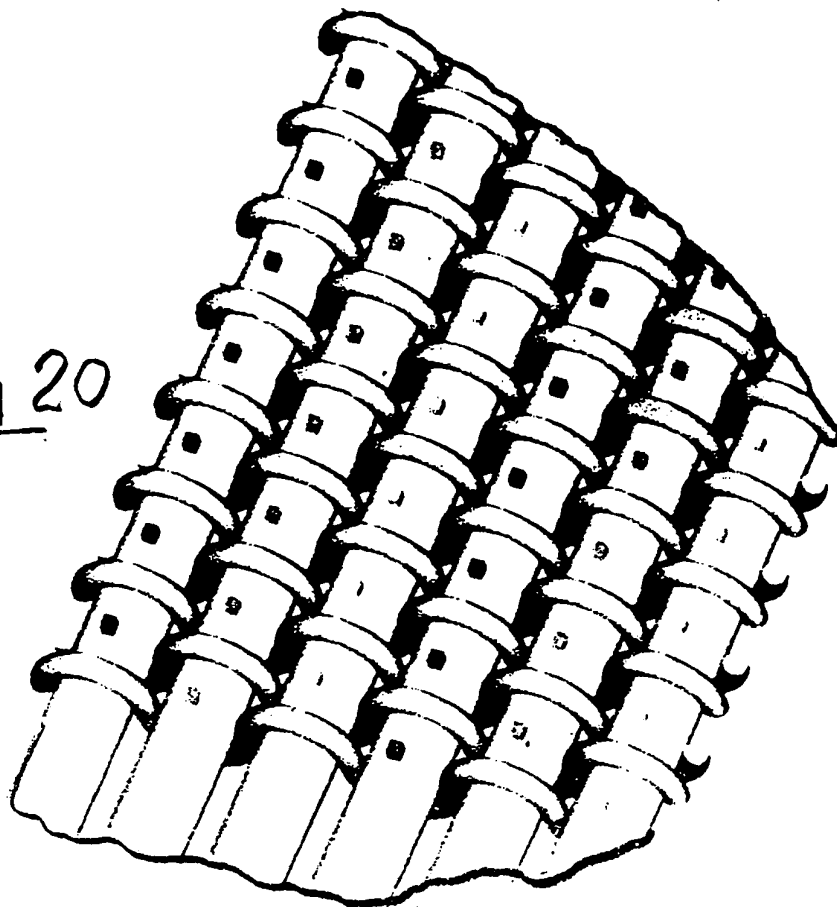
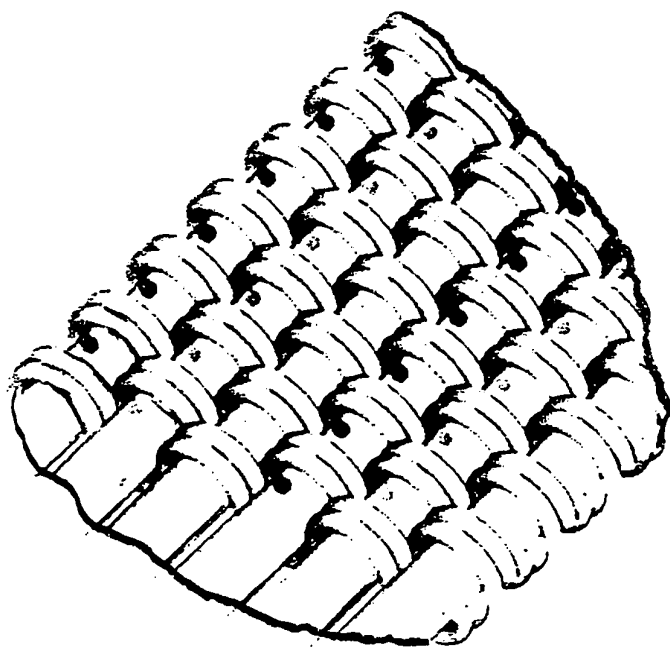


Fig 21



10-28

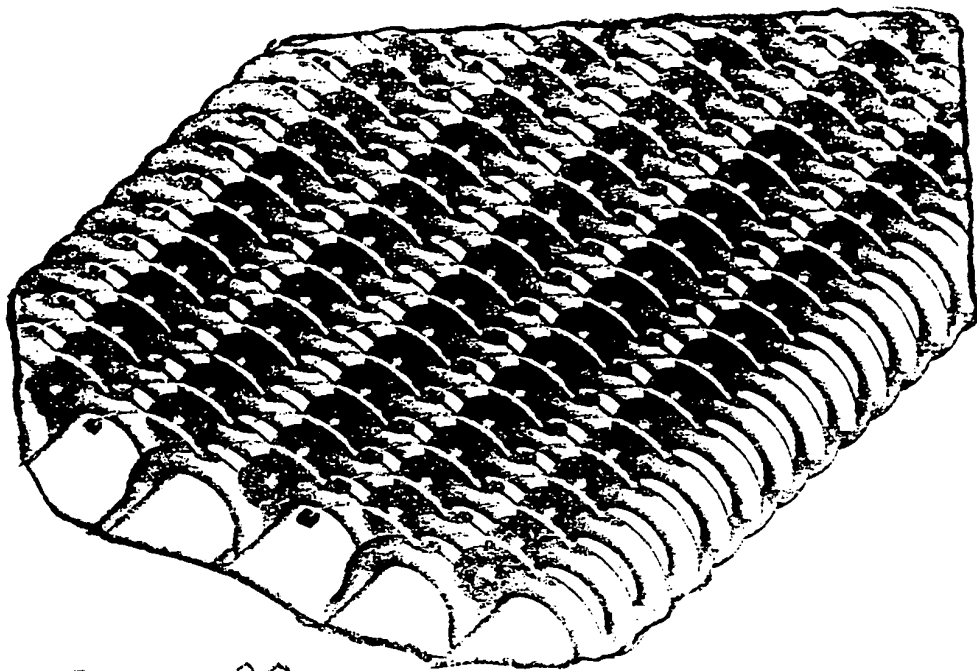


Fig 22

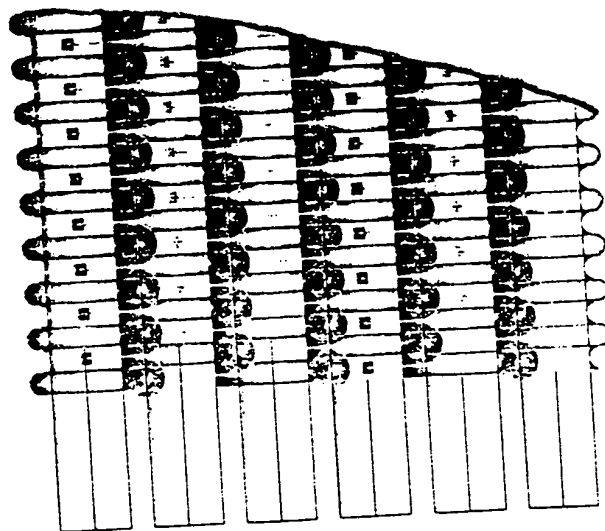
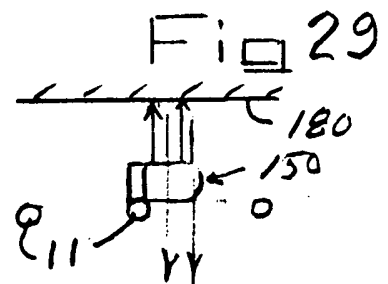
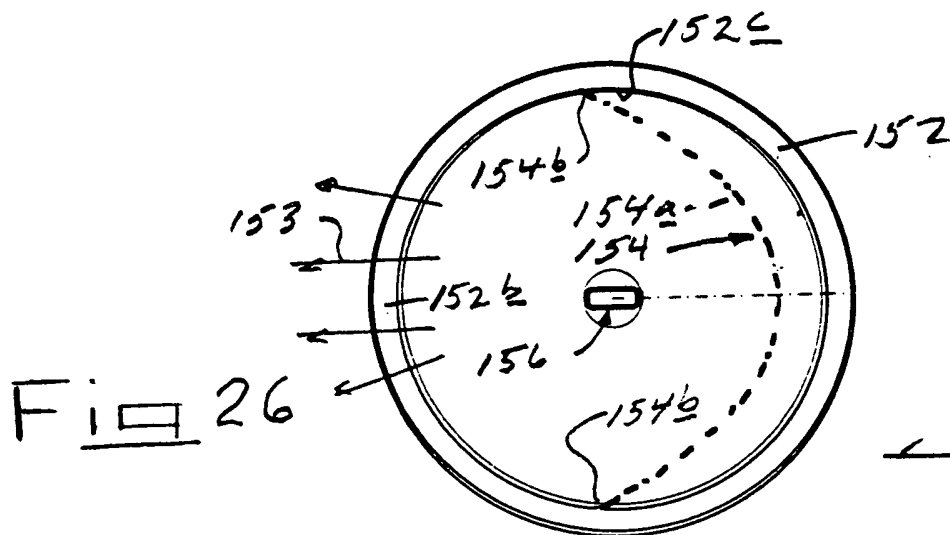
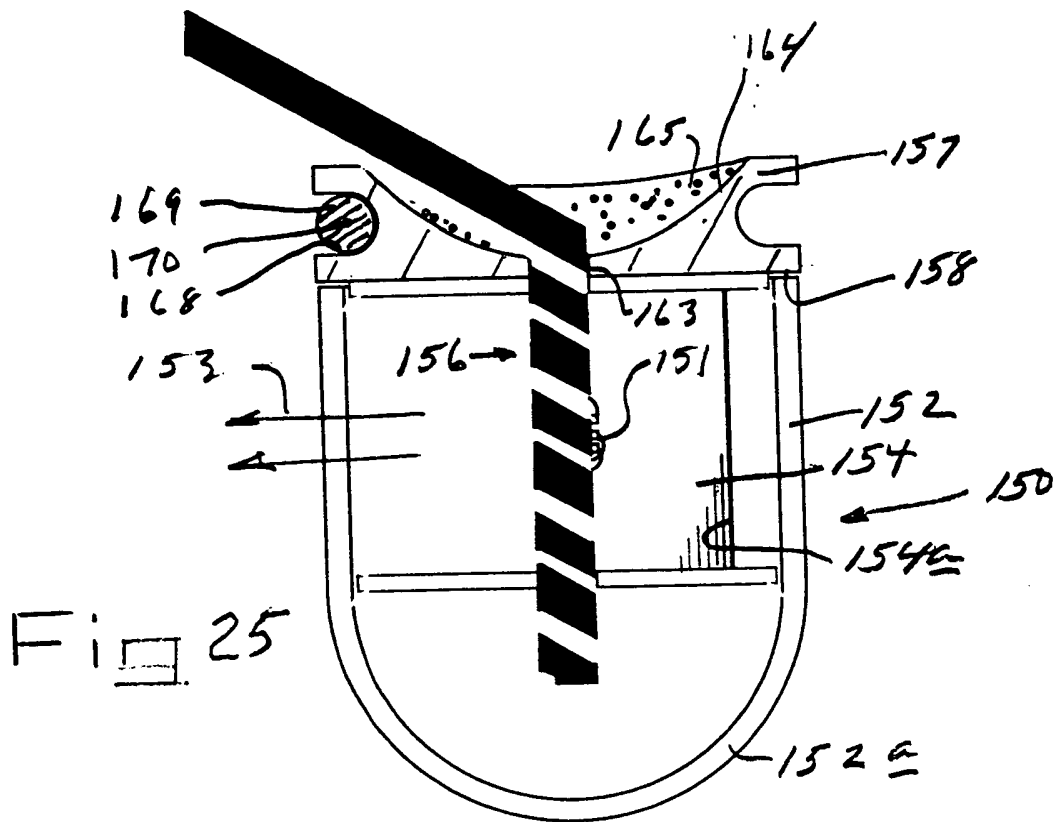


Fig 23

11-28



12-28

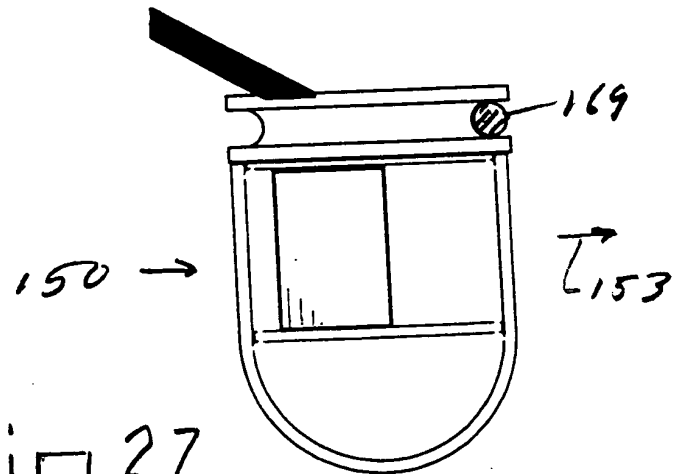
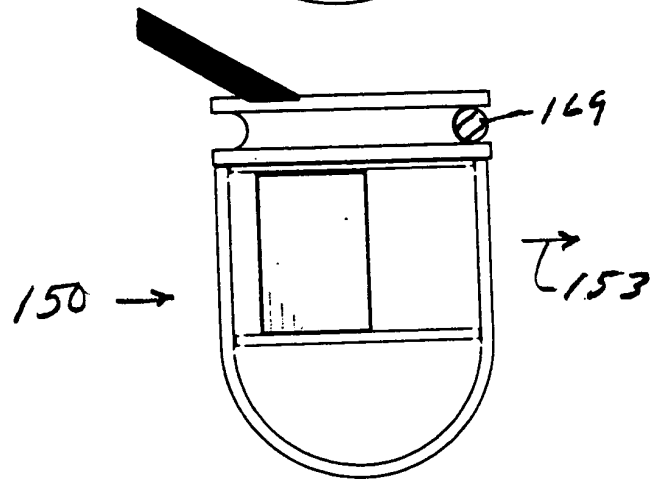
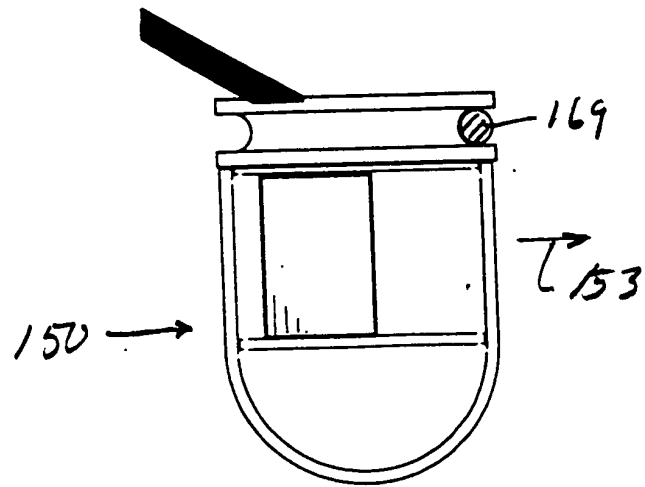


Fig 27

13-28

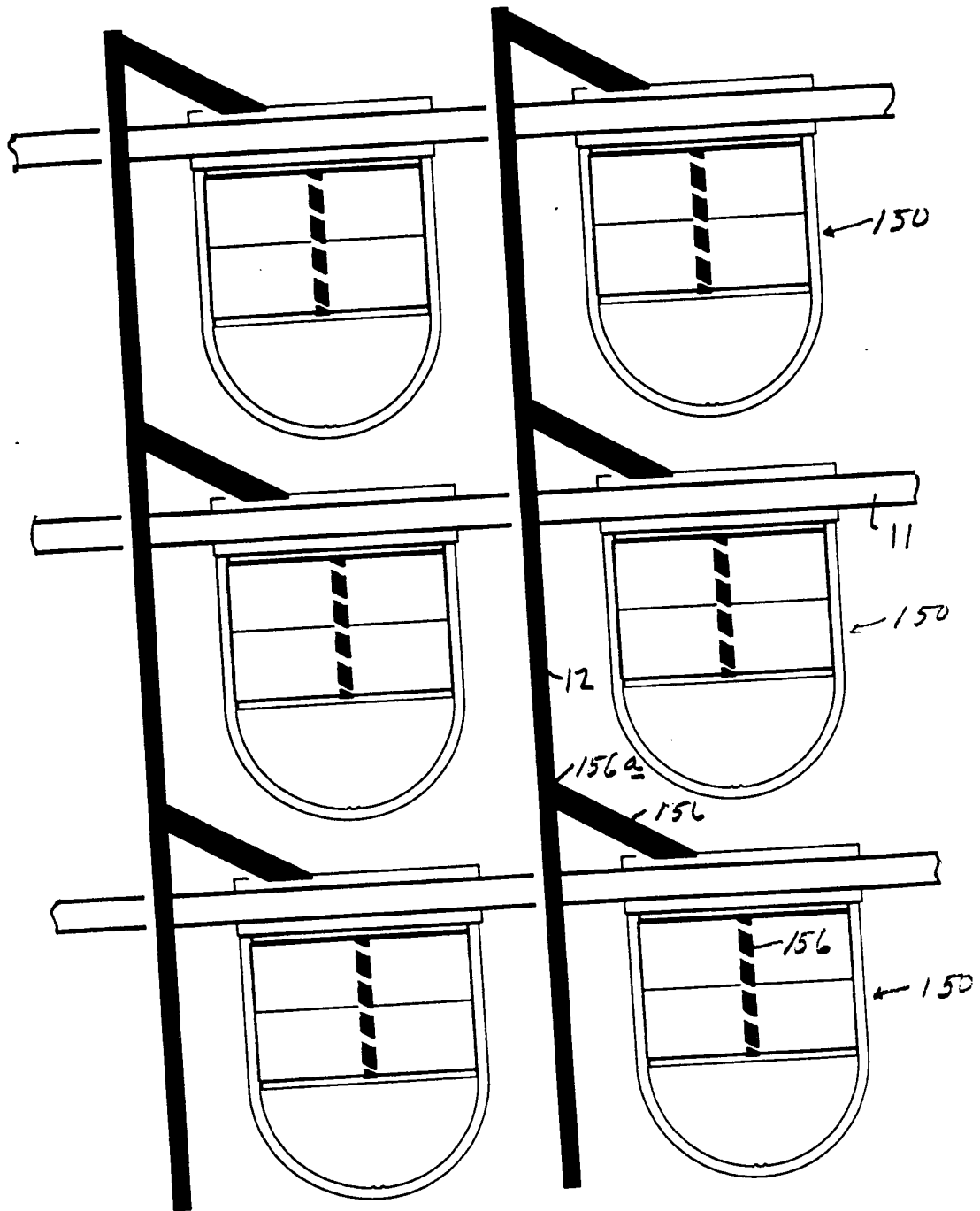


Fig 28

14-28

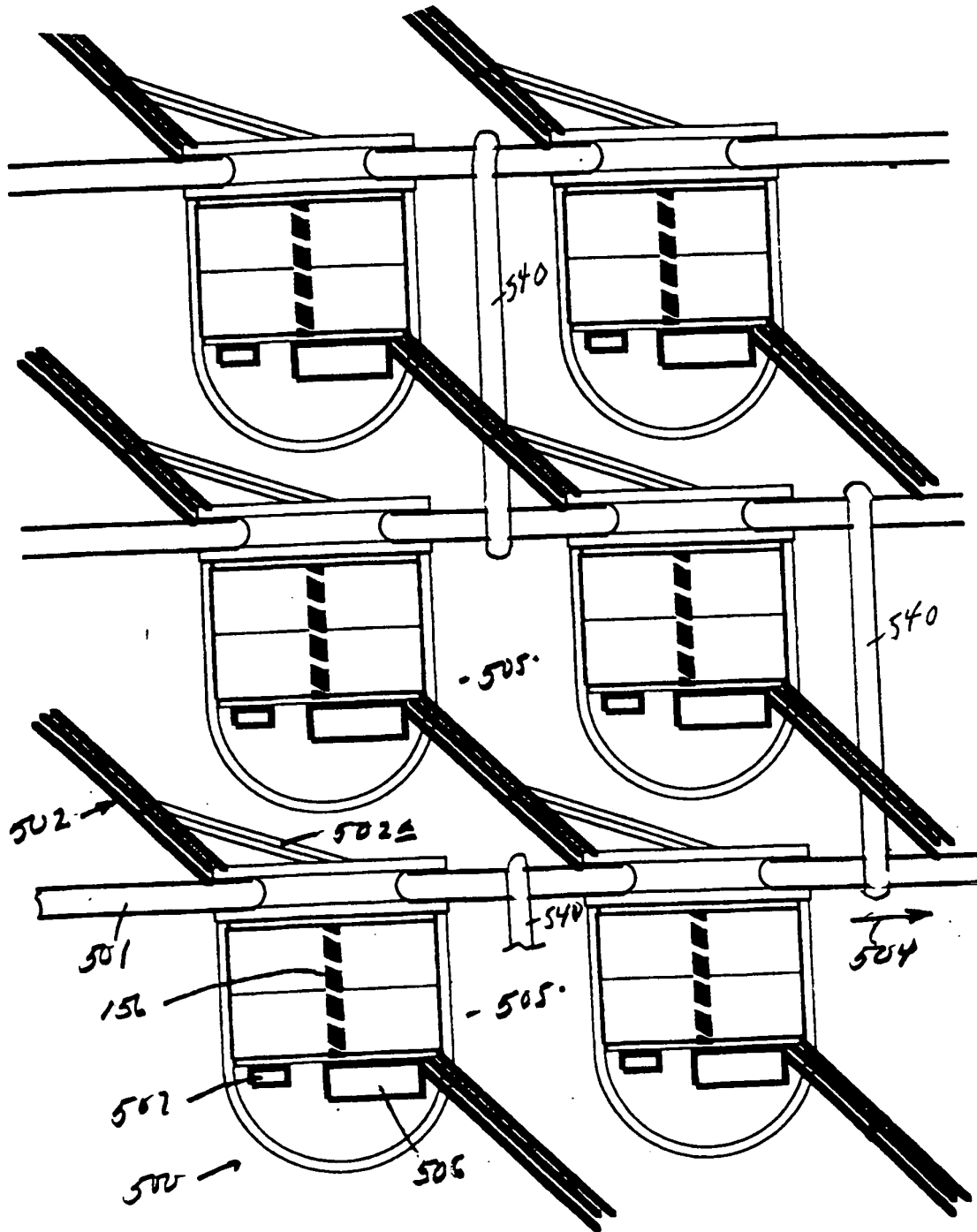
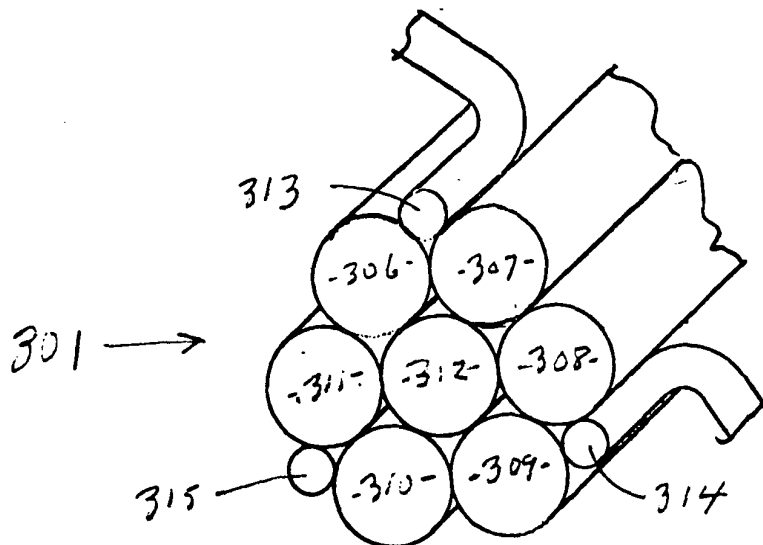
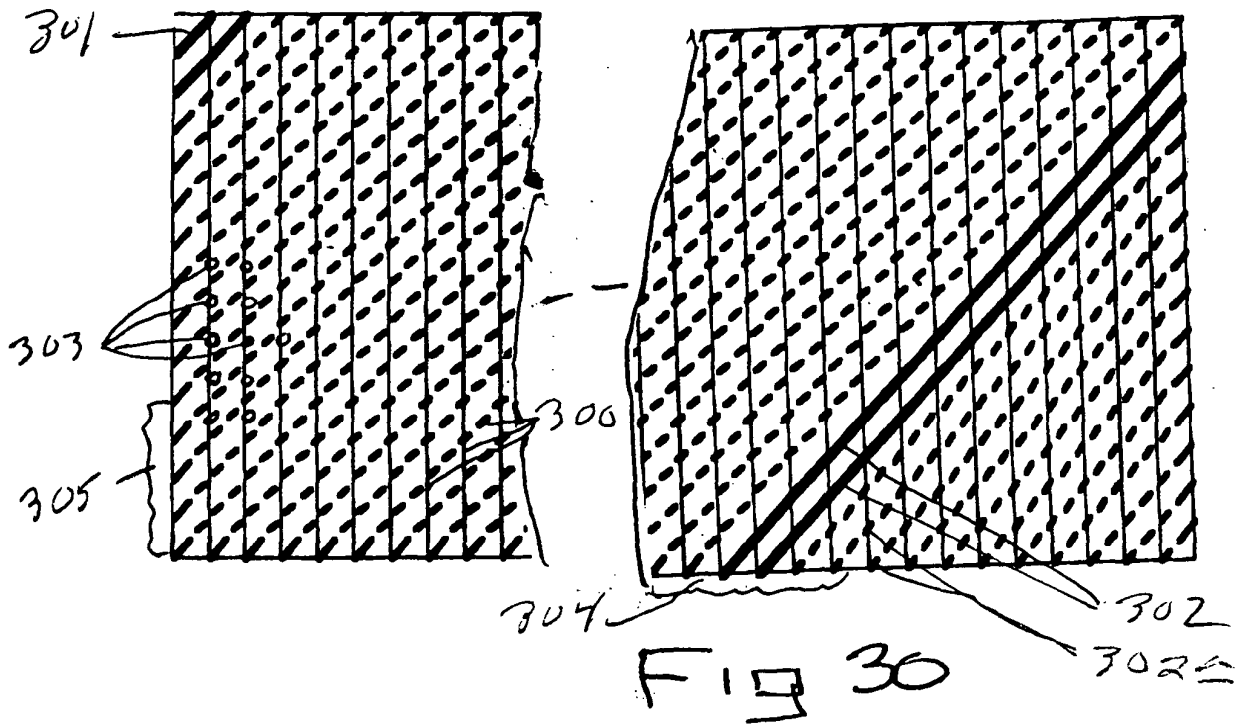
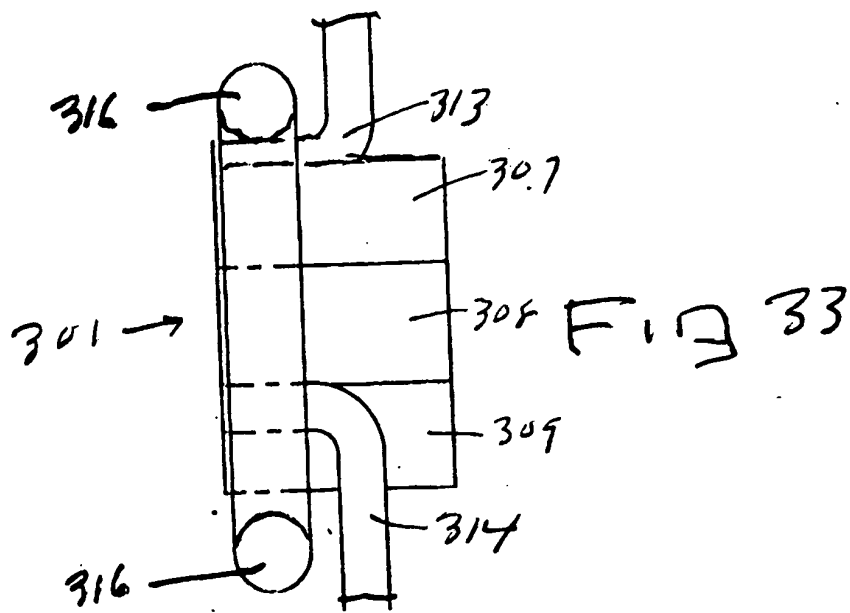
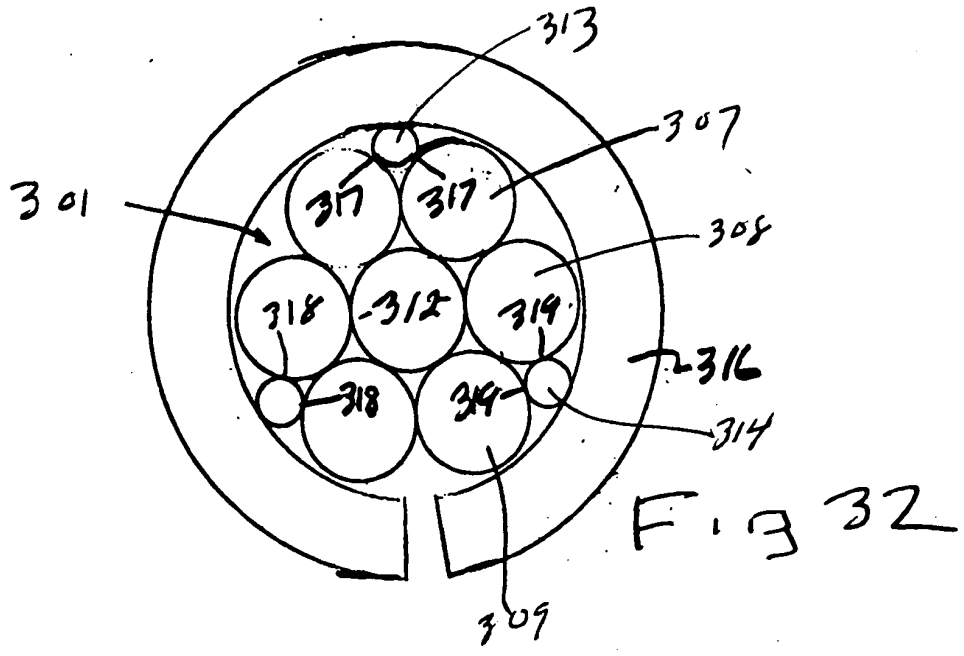


FIG 28a

15-28



16-28





17-28

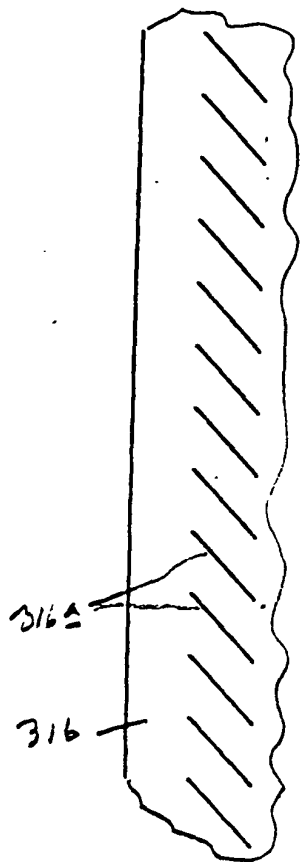


Fig 34

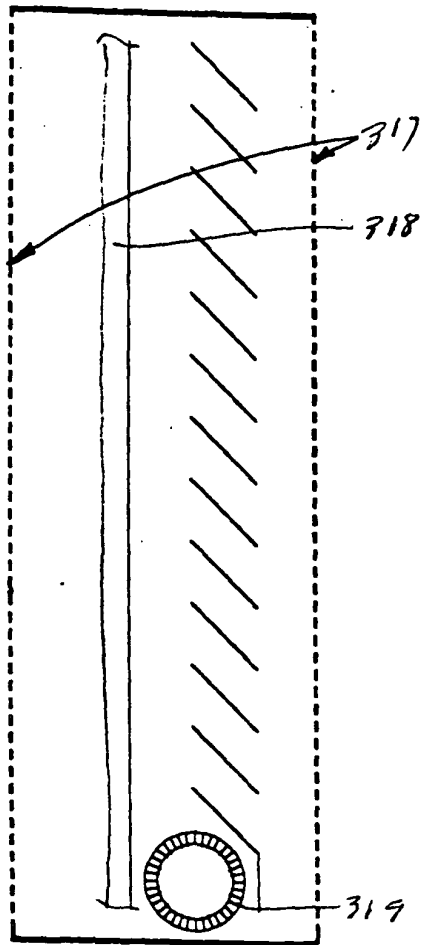
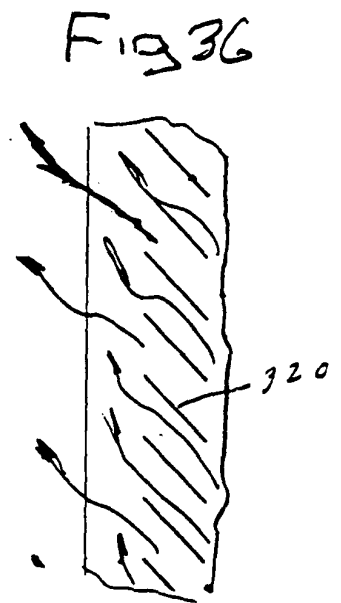


Fig 35



18-28

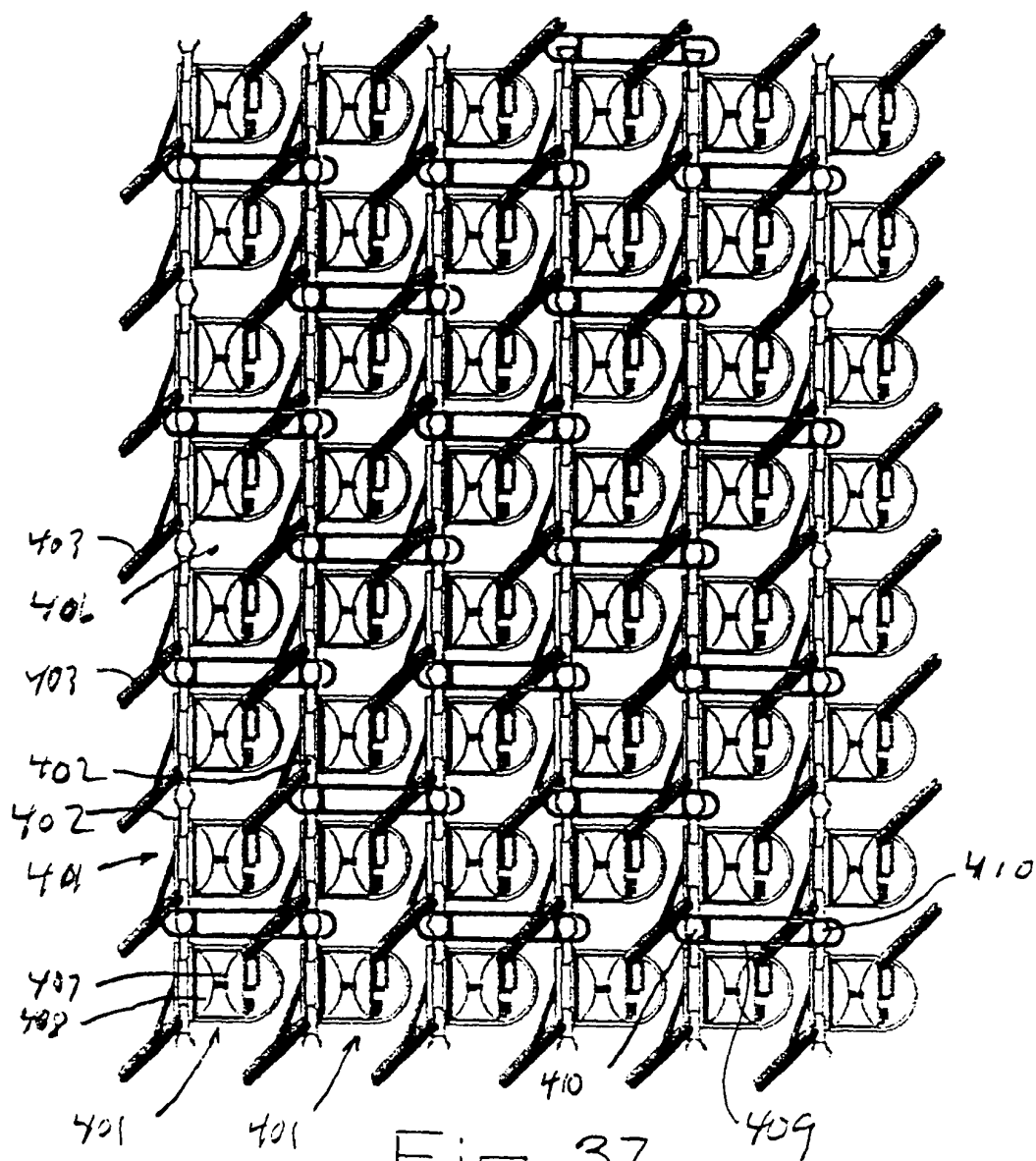


Fig 37

19-28

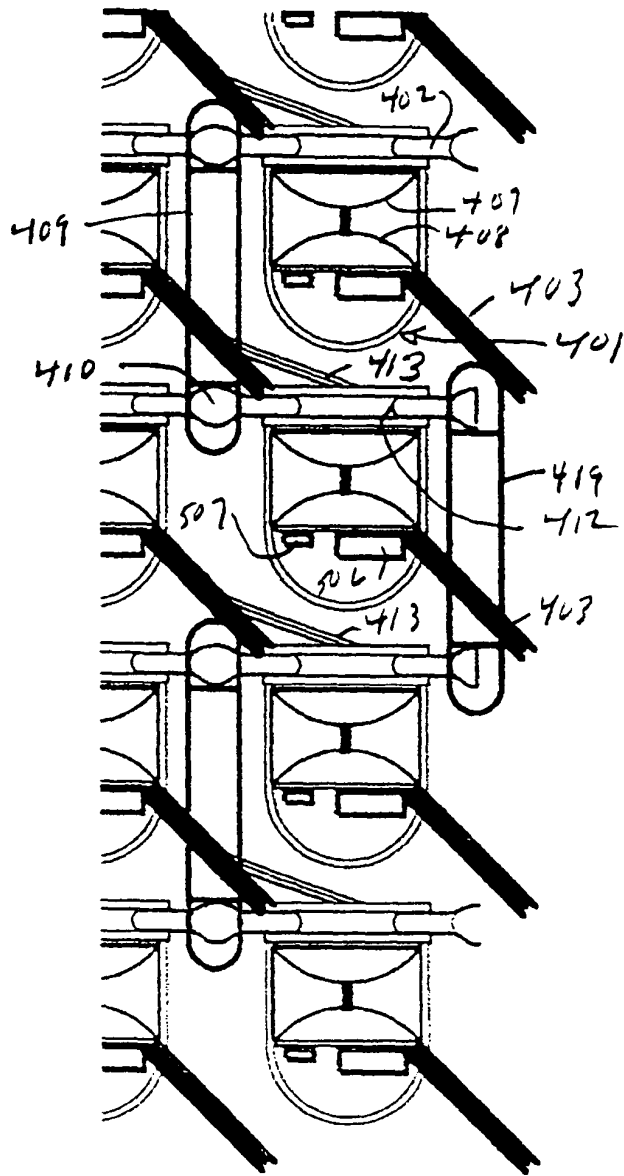


Fig 38

Fig 39

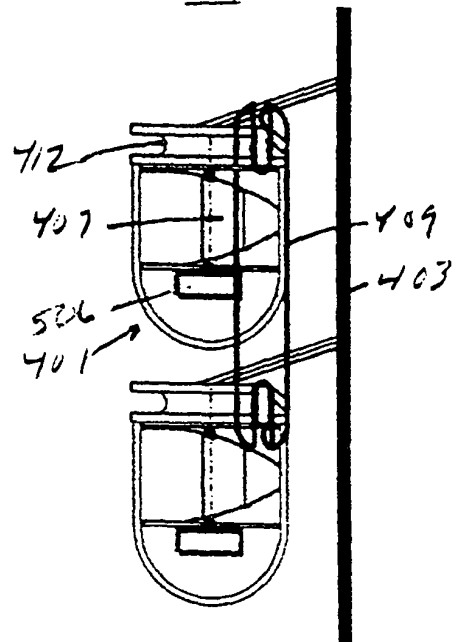


Fig 40



20-28

Fig 41

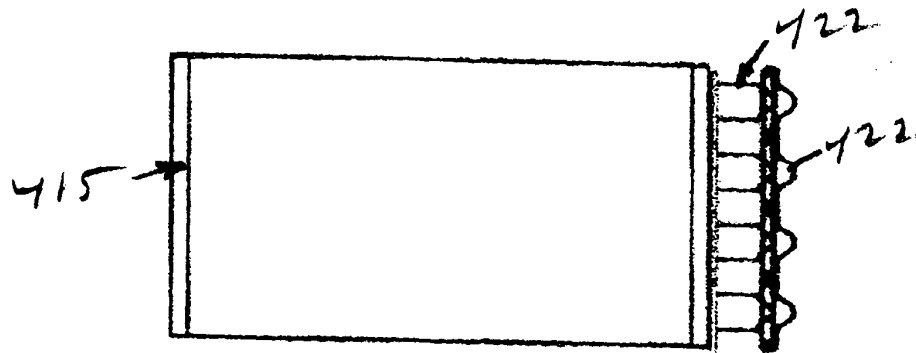
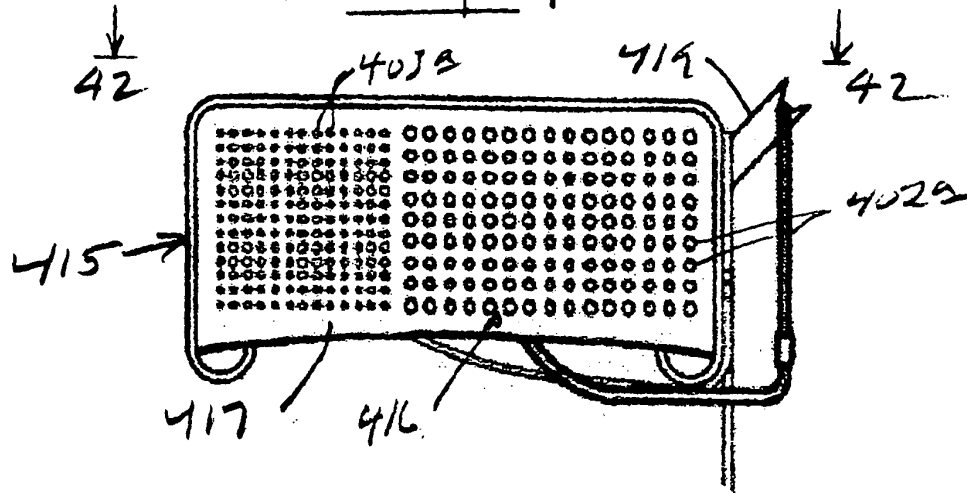


Fig 42

403 →

21-28

Fig 43

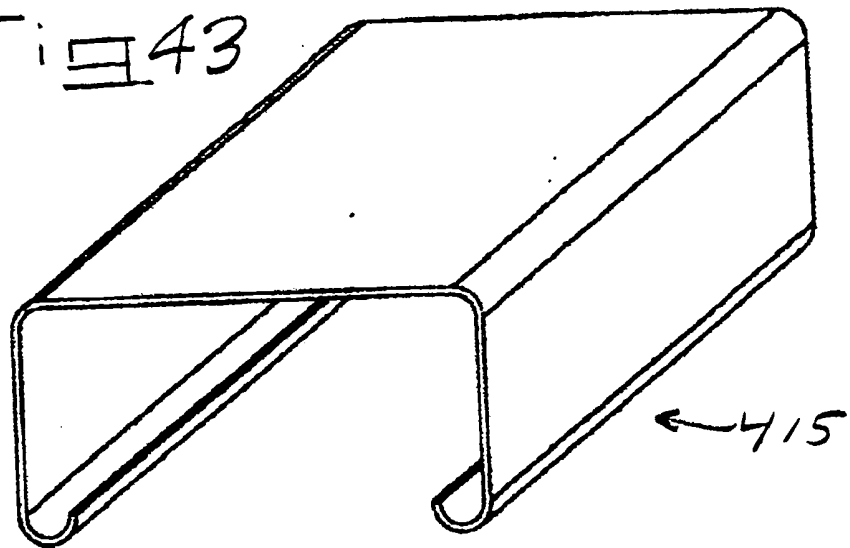
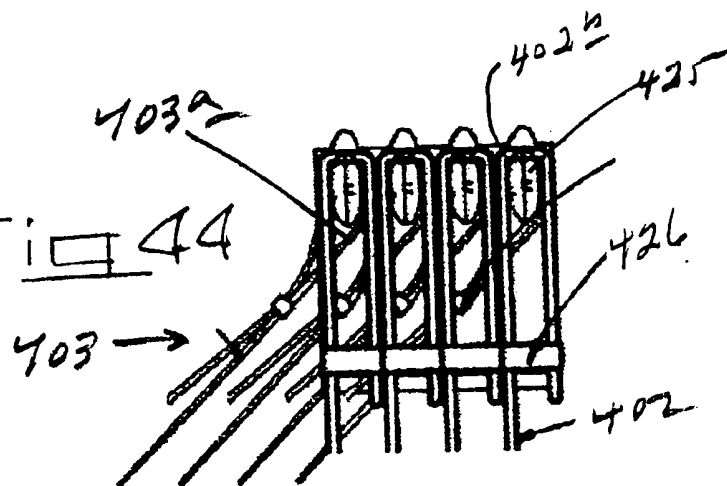


Fig 44



22-28

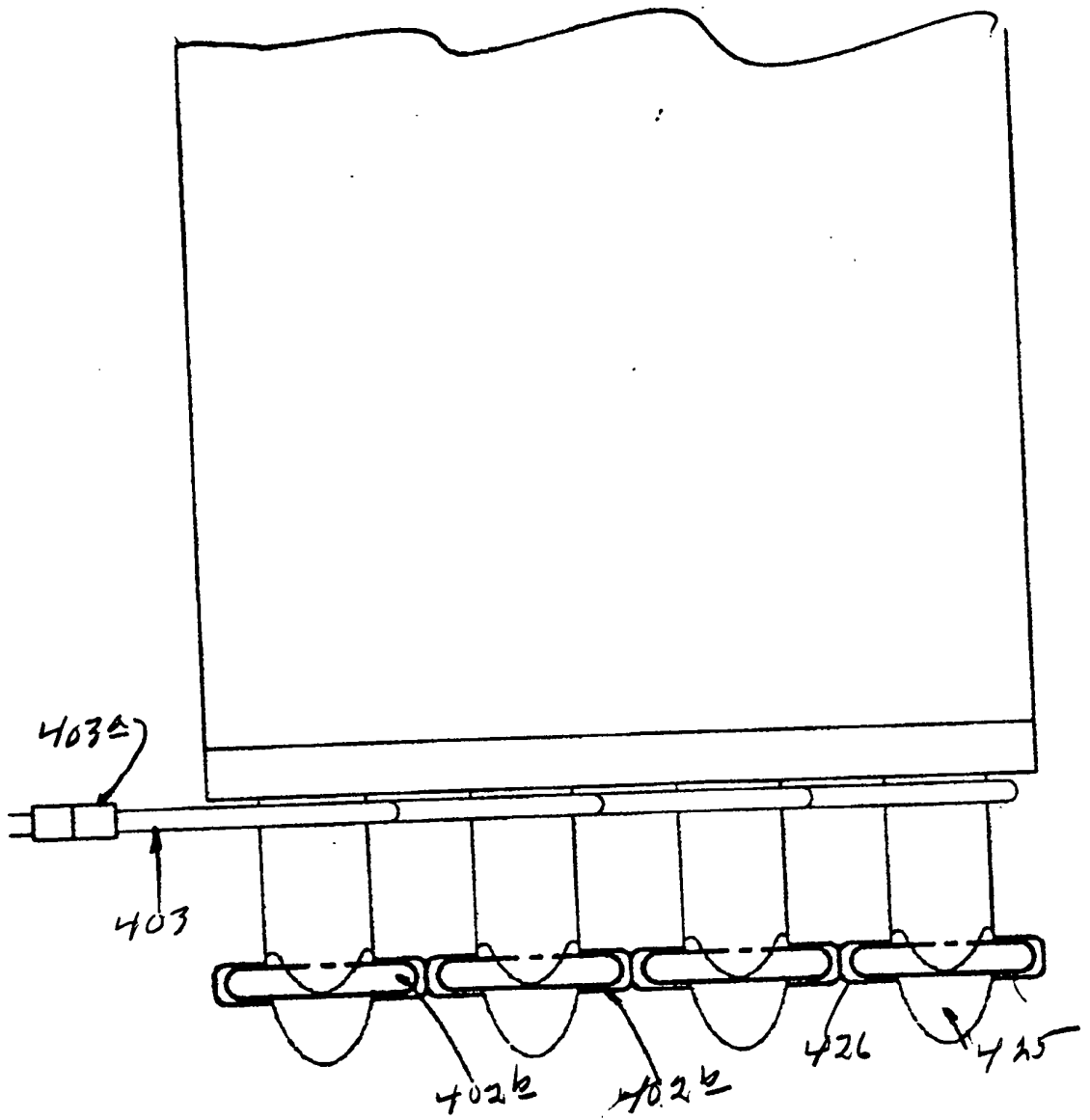
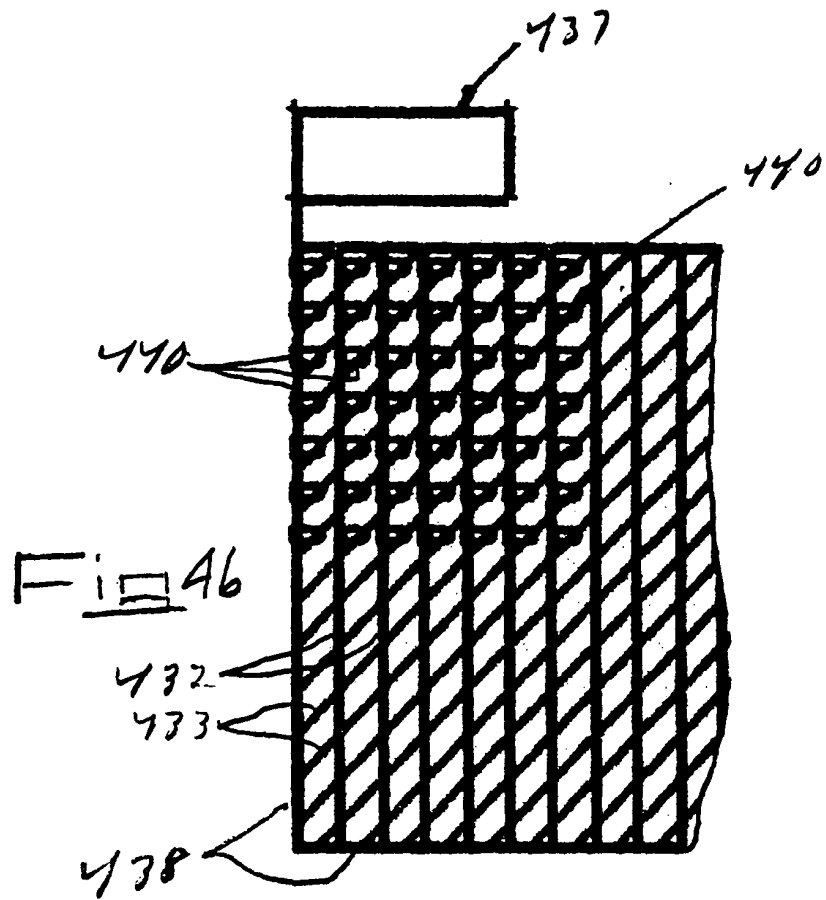


Fig 45

23-28



24-28

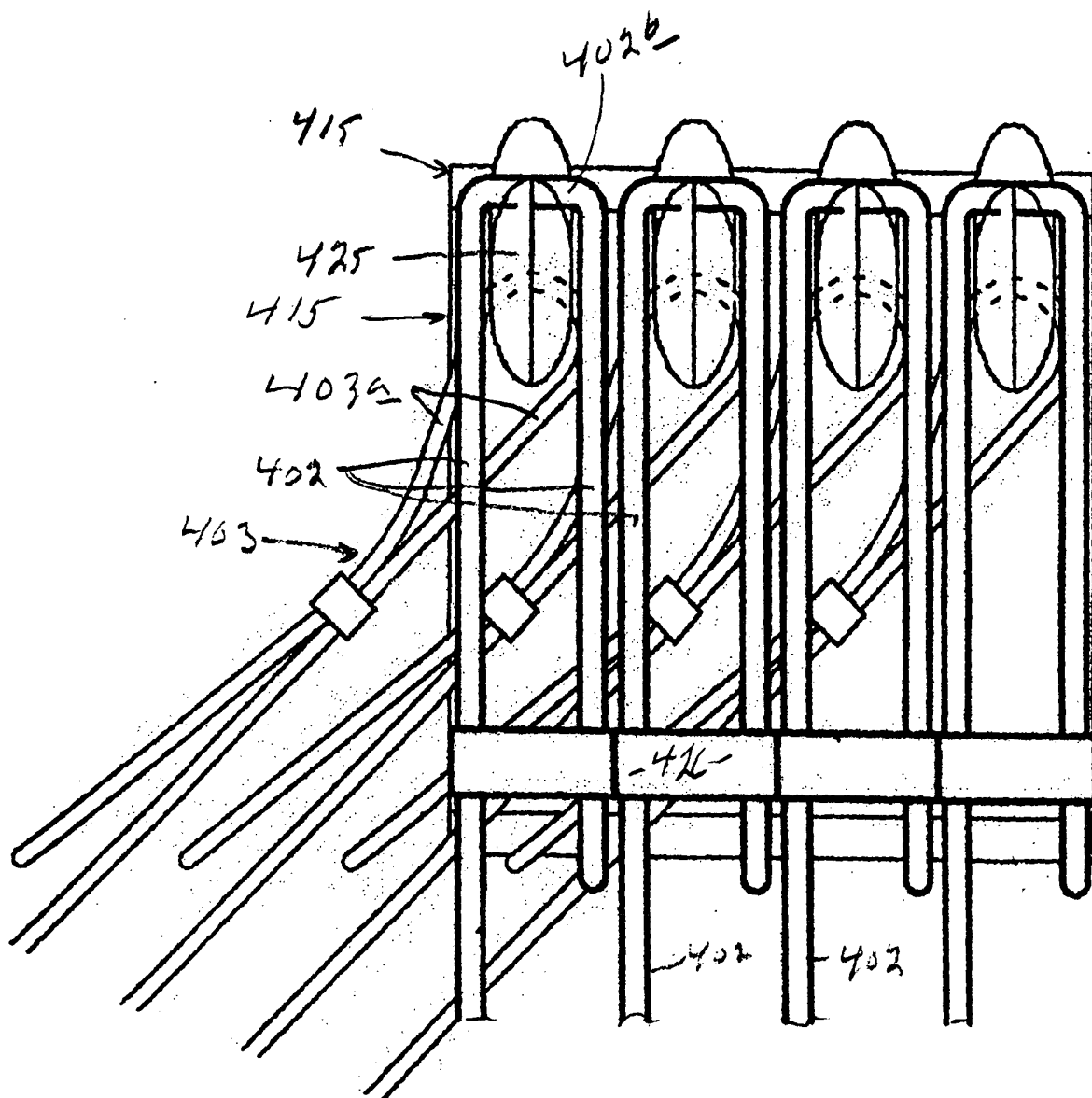
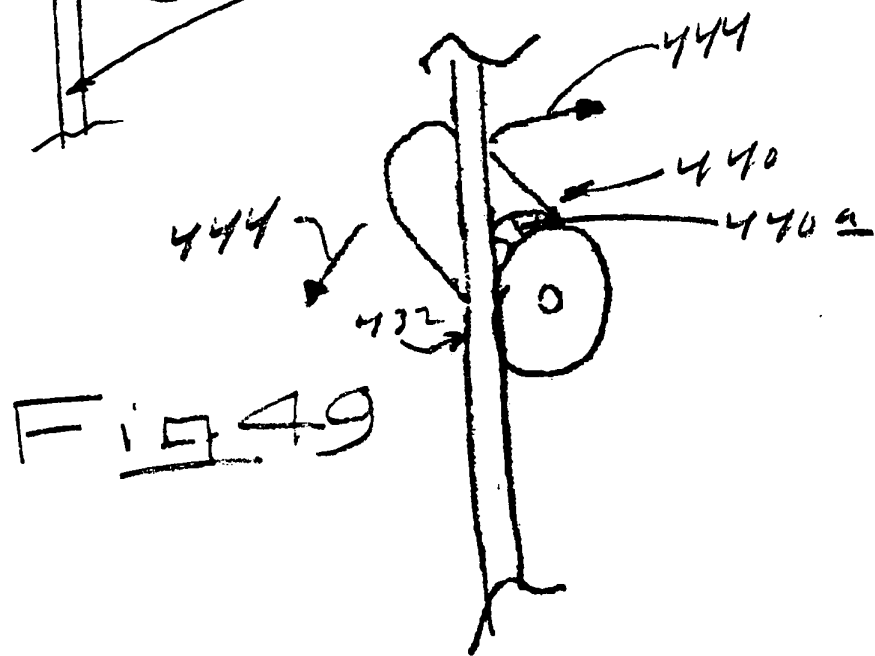
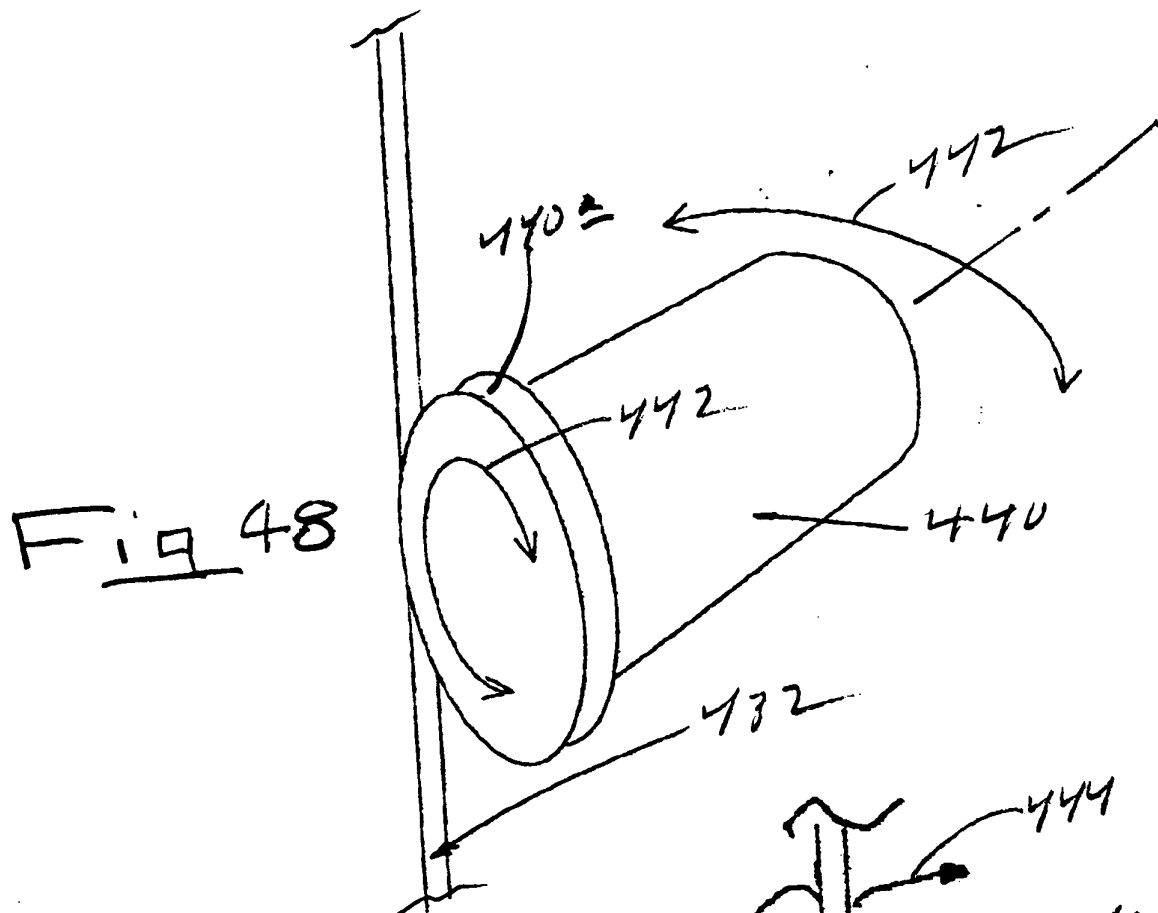


Fig 47





26-28

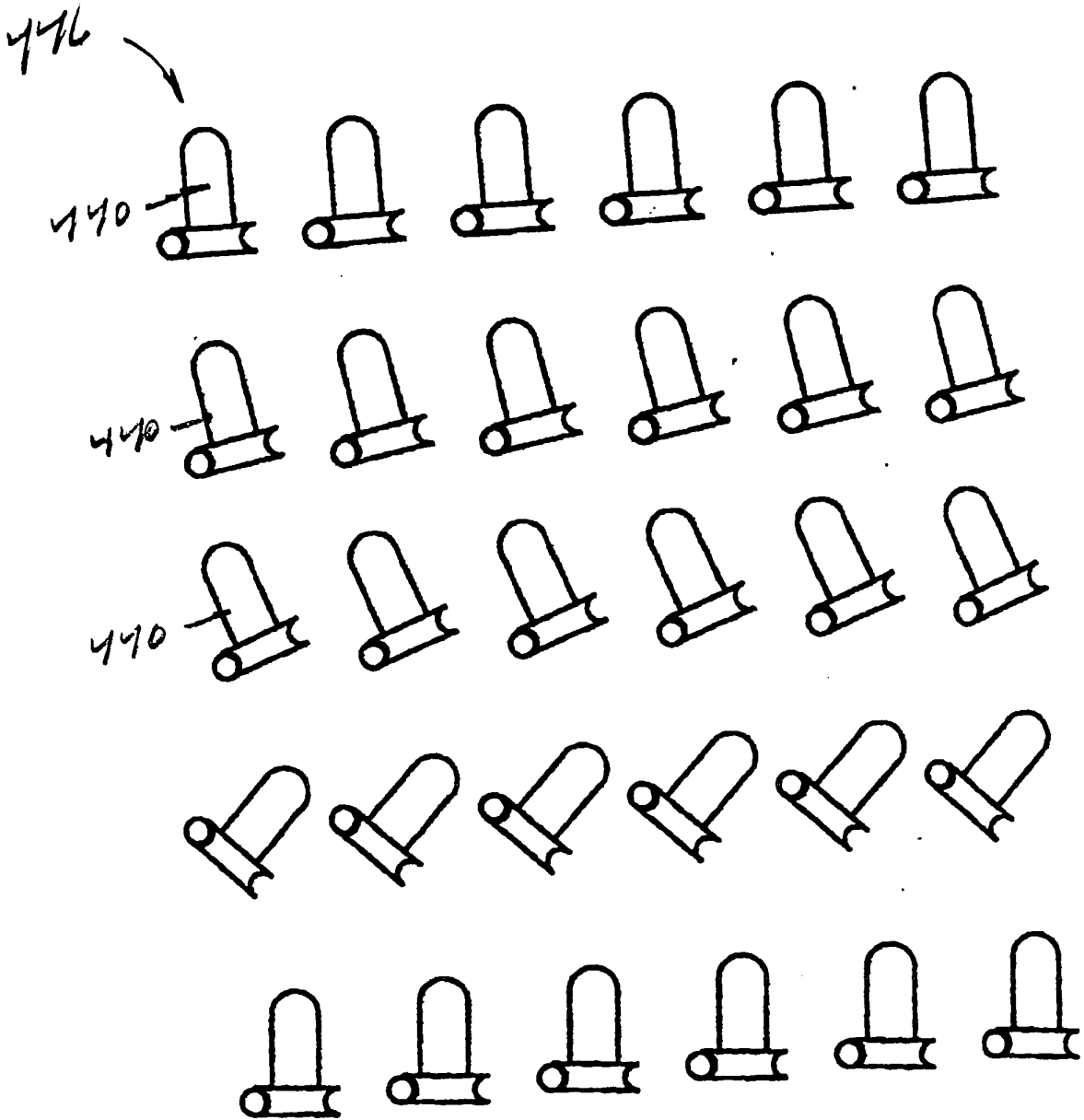
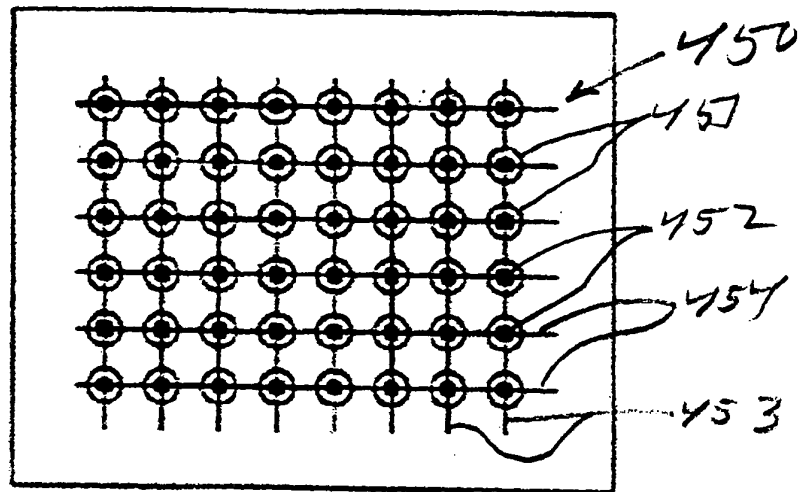


Fig 50

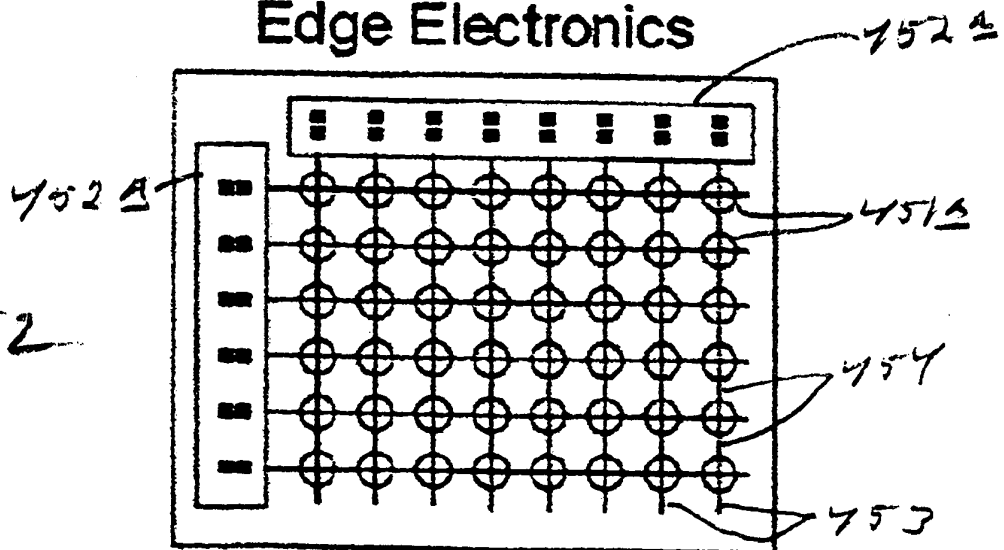
# Integrated Pixel Electronics

Fig 51



# Edge Electronics

Fig 52



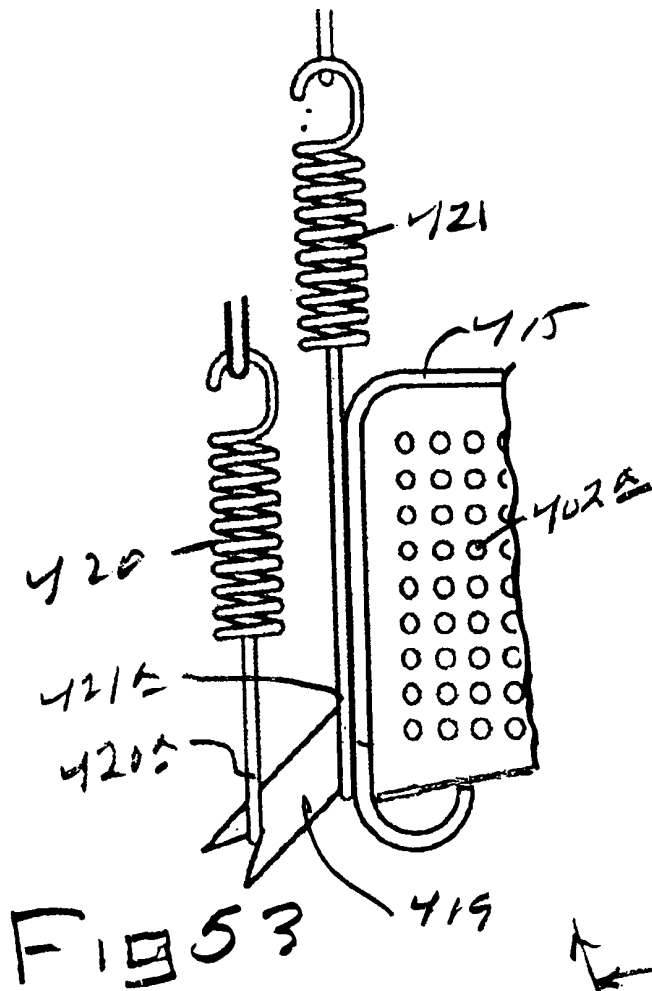


Fig 54

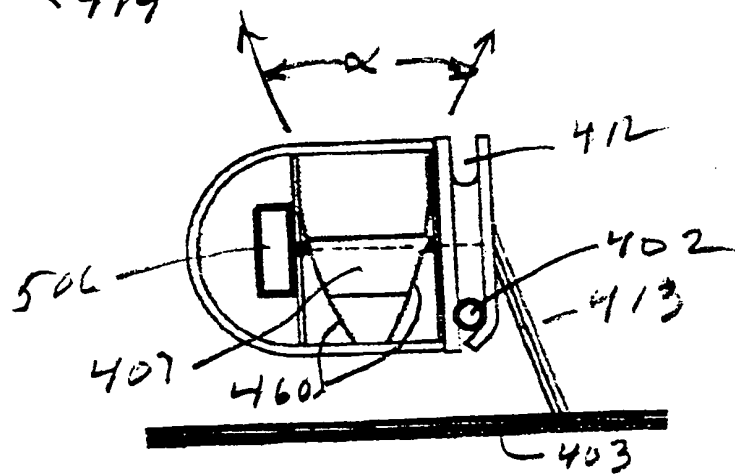
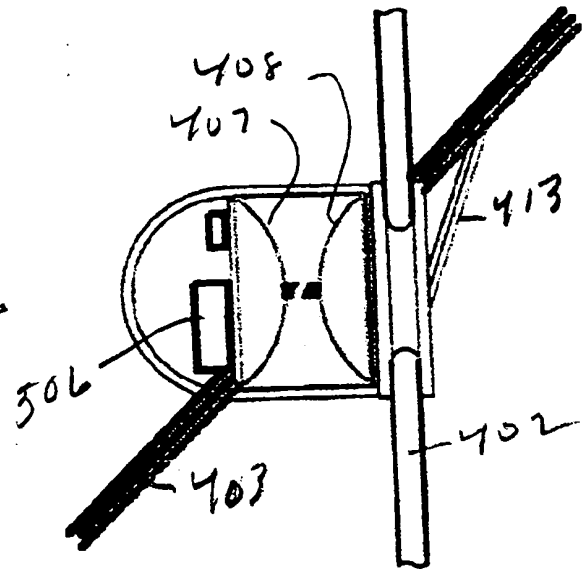


Fig 55